



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re Application of:
Sethuraman et al.

Serial No. 09/779,123

Filed: February 7, 2001

For: PLANARIZED SEMICONDUCTOR
INTERCONNECT TOPOGRAPHY AND
METHOD FOR POLISHING A METAL
LAYER TO FORM INTERCONNECT

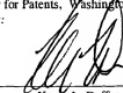
Group Art Unit: 2823
Examiner: Lee, H.

Atty. Dkt. No. 5298-02502

CERTIFICATE OF TRANSMISSION
37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: **Box Non-Fee Amendment**, Assistant Commissioner for Patents, Washington, D.C. 20231, on the date indicated below:

October 3, 2002
Date


Kevin L. Daffer

REQUEST FOR APPROVAL OF DRAWING CHANGES UNDER 37 C.F.R. §1.121

Box Non-Fee Amendment
Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Pursuant to 37 C.F.R. §1.121 (d), it is respectfully requested that the changes shown in red in the attached drawings be approved by the Examiner in the above-referenced case. The notation "Related Art" has been added to FIGS. 1-4. Support for these changes can be found within the Specification, for example, on pages 2-5. No new elements have been added to the drawings by these changes.

Accordingly, these changes do not present new matter.


Respectfully submitted,


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